

# ACB-MP.LPCI

User Manual | 5102



SEALEVEL®

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# Before You Get Started

## What's Included

The ACB-MP.LPCI adapter is shipped with the following items. If any of these items is missing or damaged, please contact Sealevel for replacement.

- **ACB-MP.LPCI adapter (Item# 5402)**

## Advisory Conventions



### **Warning**

The highest level of importance used to stress a condition where damage could result to the product, or the user could suffer serious injury.



### **Important**

The middle level of importance used to highlight information that might not seem obvious or a situation that could cause the product to fail.



### **Note**

The lowest level of importance used to provide background information, additional tips, or other non-critical facts that will not affect the use of the product.

# Introduction

The ACB-MP.LPCI adapter provides the PC with a single channel multi-protocol synchronous serial interface utilizing the Zilog Z85230 (ESCC™), which supports the HDLC/SDLC protocol using the Sealevel SeaMAC V4 device driver. SeaMAC V4 also supports certain configurations of monosync, bisync, external sync, and raw modes. The ACB-MP.LPCI adapter is an ideal solution for applications including DDS (digital data service), military applications, banking communications, and other specialized applications.

The ACB-MP.LPCI also utilizes the Sipex-506 multi-protocol electrical interface chip that allows the ACB-MP.LPCI to be compliant with EIA/TIA-530/530A, EIA/TIA-232E, EIA/TIA-485, and ITU V.35. Optional cables are available to connect RS-449, RS-530, RS-530A, V.35 and X.21 interfaces.

The ACB-MP.LPCI meets the PCISIG size requirements for an MD1 low profile adapter. This allows the ACB-MP.LPCI to be used in a variety of modern systems, including low profile servers, network appliances and 'All in One' desktop computers. A standard height bracket is available for implementations in a standard height PCI slot. The ACB-MP.LPCI is a 'Universal Bus' adapter, allowing for use in either +5V or +3.3V PCI slots and provides compliance with the PCISIG PCI Bus Specification Revision 2.2/2.3, which requires ATX power support for both 5V and 3.3V.

## Other Sealevel Sync Products

<b>Low Profile PCI – 1- Port</b>		
ACB-232.LPCI	(P/N: 5103)	Z85230 RS-232
ACB-Ultra.LPCI	(P/N: 5104)	Z16C32 RS-232/422/485/530/530a/V.35
<b>PCI – 1 – Port</b>		
ACB-232.LPCI	(P/N: 5103S)	Z85230 RS-232
ACB-Ultra.LPCI	(P/N: 5104S)	Z16C32 RS-232/422/485/530/530a/V.35
<b>PCI – 4 – Port</b>		
ACB-MP+4.PCI	(P/N: 5402)	Z85230 RS-232/422/485/530/530/V.35
<b>PC/104 – 1- Port</b>		
ACB-104	(P/N: 3512)	Z85230 RS-232/422/485/530/530a/V.35
ACB-104.ULTRA	(P/N: 3514)	Z16C32 RS-232/422/485/530/530 <sup>a</sup> /V.35
<b>PCMCIA – 1 – Port</b>		
PC-ACB-MP	(P/N: 3612)	Z85233 RS-232/422/482/530/530z/V.35

## Optional Items

Depending upon your application, you are likely to find one or more of the following items useful for interfacing the ACB-MP.LPCI to real-world signals. All items can be purchased from our website (<http://www.sealevel.com>) or by calling 864-843-4343.

<b>DB25 Female to DB25 Male Extension Cable, 72 inch Length (Item# CA104)</b>	
<p>The CA104 is a standard DB25F to DB25M serial extension cable. Extend a DB25 cable or locate a piece of hardware where it is needed with this six foot (72 inch) cable. The connectors are pinned one-to-one, so the cable is compatible with any device or cable with DB25 connectors. The cable is fully shielded against interference and the connectors are molded to provide strain relief. Dual metal thumbscrews secure the cable connections and prevent accidental disconnection. Recommended for applications with data rates less than 1M bps.</p>	
<b>DB25 Female (RS-530) to DB37 Male (RS-449 DTE) Cable, 10 inch Length (Item# CA107)</b>	
<p>DB25 Female (RS-530) to DB37 Male (RS-449 DTE) Cable, 10 inch Length. RS-530 was designed to replace the bulky DB37 RS-449 connector. The CA107 cable allows any Sealevel RS-530 adapter to be used in an RS-449 application.</p>	
<b>DB25 Female (RS-530) to DB15 Male (X.21) Cable, 72 inch Length (Item# CA159)</b>	
<p>DB25 Female (RS-530) to DB15 Male (X.21) Cable, 72 inch Length. Converts the Standard DB25 implementation of RS-530 or RS-422 to the ITU-T X.21 standard pinout.</p>	
<b>DB25 Female to DB25 Male (RS-530) Twisted Pair Serial Cable, 72 inch Length (Item# CA174)</b>	
<p>DB25 Female to DB25 Male Twisted Pair Serial Cable, 72 inch Length. Twisted Pairs provide increased data integrity in high-speed serial applications. Recommended for RS-530 applications with data rates greater than 1M bps.</p>	
<b>DB25 Female (V.35) to ITU-T ISO-2593 Style Connector (V.35) Cable, 72 inch Length (Item# CA178)</b>	
<p>DB25 Female (V.35) to ITU-T ISO-2593 Style Connector (V.35) Cable, 72 inch Length. The CA178 converts the Sealevel DB25 implementation of V.35 to the ITU-T V.35 mechanical standard.</p>	

# Installation & Configuration

## Software Installation

### Windows Installation



Do not install the Adapter in the machine until the software has been fully installed.



Only users running Windows 7 or newer should utilize these instructions for accessing and installing the appropriate driver via Sealevel's website. If you are utilizing an operating system prior to Windows 7, please contact Sealevel by calling 864.843.4343 or emailing [support@sealevel.com](mailto:support@sealevel.com) to receive access to the proper driver download and installation instructions.

1. Begin by locating, selecting, and installing the correct software from the [Sealevel software driver database](#).
2. Type in or select the part number (**#5102**) for the adapter from the listing.
3. Select "Download Now" for SeaMAC for Windows.
4. The setup files will automatically detect the operating environment and install the proper components. Follow the information presented on the screens that follow.
5. A screen may appear with text similar to: "The publisher cannot be determined due to the problems below: Authenticode signature not found." Please click the 'Yes' button and proceed with the installation. This declaration simply means that the operating system is not aware of the driver being loaded. It will not cause any harm to your system.
6. During setup, the user may specify installation directories and other preferred configurations. This program also adds entries to the system registry that are necessary for specifying the operating parameters for each driver. An uninstall option is also included to remove all registry/INI file entries from the system.
7. The software is now installed, and you can proceed with the hardware installation.

For additional software support, please call Sealevel Systems' Technical Support, (864) 843-4343. Our technical support is free and available from 8:00 AM - 5:00 PM Eastern Time, Monday through Friday. For email support contact: [support@sealevel.com](mailto:support@sealevel.com).

# Physical Installation

The adapter can be installed in any PCI (5V or 3.3V) expansion slot.



Do not install the adapter until the software has been successfully installed.

1. **Turn off power. Disconnect the power cord.**
2. Remove the PC case cover.
3. Locate an available PCIe slot and remove the blank metal slot cover.
4. Gently insert the PCIe adapter into the slot. Make sure the adapter is seated properly.
5. Replace the screw (required for FCC Part 15 compliance).
6. Replace the cover.
7. Connect the power cord and power up the machine.
8. The ACB-MP.LPCI is now ready for use.

# Technical Description

The ACB-MP.LPCI utilizes the Zilog 85230 Enhanced Serial Communications Controller (ESCC). This chip features programmable baud rate, data format and interrupt control. Refer to the ESCC User Manual, available from Zilog, for details on programming the Z85230 ESCC chip in the event you are writing your own driver as opposed to using the SeaMAC driver.

## Internal Baud Rate Generator

The baud rate of the ESCC is programmed under software control. The standard oscillator supplied with the board is 7.3728 MHz. However, other oscillator values can be substituted to achieve different baud rates.

## Control and Status Registers Definition

The control and status registers occupy 16 consecutive I/O locations. The following tables provide a functional description of the bit positions. This table is provided for users that desire to write their own driver.

X = do not care.

Base	Mode	D7	D6	D5	D4	D3	D2	D1	D0
+4	RD	0	IRQST	0	0	0	0	0	DSRA
+4	WR	X	X	X	0	X	X	X	X
+5	RD	485CLK	ECHOA	SYNCA_RTS	SYNCA_CTS	AM3	AM2	AM1	AM0
+5	WR	485CLK	ECHOA	SYNCA_RTS	SYNCA_CTS	AM3	AM2	AM1	AM0
+6	RD	0	0	0	0	RLA	LLA	TSETSLA	RXCOPTA
+6	WR	X	X	X	X	RLA	LLA	TSETSLA	RXCOPTA
+14	RD	SD7	SD6	SD5	SD4	SD3	SD2	SD1	SD0
+15	RD	SD15	SD14	SD13	SD12	SD11	SD10	SD9	SD8



Field	Description		
IRQST	SCC interrupt status:	1 = No interrupt pending on ESCC	0 = Interrupt pending on ESCC.
DSRA	DSRA:	1 = DSRA not active	0 = DSRA active
LLA	Local Loopback:	1 = LL set	0 = LL not set
RLA	Remote Loopback:	1 = RL set	0 = RL not set
TSETSLA	TSET clock source:	1 = Received TXC as source	0 = TRXCA as source
RXCOPTA	RXCOPTA:	1 = Selects SCC PCLK for RTXCA	0 = Selects received RXC for RTXCA
SYNCA_RTS	SYNCA_RTS:	1 = SYNCA connected to RTS	0 = SYNCA is high
SYNCA_CTS	SYNCA_CTS:	1 = SYNCA connected to CTS	0 = SYNCA is high
485CLK	TSET switches with TXD:	1 = clk switches	0 = no CLK switching
ECHOA	ECHO enable:	1 = echo disabled	0 = echo enabled
AM0 – AM3	I/O mode select. See table for valid interface options.		0 = High Impedance
SD0 – SD15	Optional security feature. Unique value per customer or application.		Default value = FFFF

## Interface Selection

The ACB-MP.LPCI supports a variety of electrical interfaces. Refer to the Control and Status Register Definitions found in the Technical Description section of this manual for this bit description. There is line termination on RXD, RXC, and TXC in the following modes: RS-530, RS-530A, RS-485T, and V.35.

HEX	M3	M2	M1	M0	Interface Mode
0	0	0	0	0	All signals are high impedance
1	0	0	0	1	* not supported *
2	0	0	1	0	RS-232
3	0	0	1	1	* not supported *
4	0	1	0	0	RS-485T with 120 $\Omega$ termination
5	0	1	0	1	RS-485 without termination
6,7,8,9	0	1	1	0	* not supported *
A	1	0	1	0	single ended loopback
B	1	0	1	1	differential loopback
C	1	1	0	0	* not supported *
D	1	1	0	1	RS-530
E	1	1	1	0	V.35
F	1	1	1	1	RS-530A

## DB-25M Pin Connector Pin Outs

## RS-232 Signals

Base+5, M3-M0=2, 0010

Pin #	Signal	Name	Mode
2	TD	Transmit Data	Output
3	RD	Receive Data	Input
4	RTS	Request To Send	Output
5	CTS	Clear To Send *	Input
6	DSR	Data Set Ready	Input
7	GND	Ground	
8	DCD	Data Carrier Detect	Input
15	TXC	Transmit Clock	Input
17	RXC	Receive Clock	Input
18	LL	Local Loopback	Output
20	DTR	Data Terminal Ready	Output
21	RL	Remote Loopback	Output
24	TSET	Transmit Signal Element Timing	Output
25	TM	Test Mode	Input



In External Sync Mode, the external sync signal is fed into CTS.

## RS-485 or RS-485T

Base+5, M3-M0=4, 0100 (With termination)

Base+5, M3-M0=5, 0101 (Without termination)

Pin #	Signal	Name	Mode
2	TDA TX-	Transmit Negative	Output
3	RDA RX-	Receive Negative	Input
7	GND	Ground	
9	RXCB RXC+	Receive Clock Positive	Input
11	TSETB TSET+	Transmit Signal Element Timing +	Output
12	TXCB TXC+	Transmit Clock Positive	Input
14	TDB TX+	Transmit Positive	Output
15	TXCA TXC-	Transmit Clock Negative	Input
16	RDB RX+	Receive Positive	Input
17	RXCA RXC-	Receive Clock Negative	Input
18	LL	Local Loopback	Output
20	DTRA DTR-	Data Terminal Ready Negative	Output
21	RL	Remote Loopback	Output
23	DTRB DTR+	Data Terminal Ready Positive	Output
24	TSETA TSET-	Transmit Signal Element Timing -	Output



The RX+/- lines do not have pull up or pull down biasing. For high baud rates or long distances, add 1K  $\Omega$  resistor from RX- to GND, and add 820  $\Omega$  resistor from RX+ to +5V DTR (depending on state of DTR in application – do not toggle DTR).

## RS-530 (RS-422)

Pin #	Signal	Name	Mode
2	TDA TX-	Transmit Negative	Output
3	RDA RX-	Receive Negative	Input
4	RTSA RTS-	Request To Send Negative	Output
5	CTSA CTS-	Clear To Send Negative *	Input
6	DSRA DSR-	Data Set Ready Negative	Input
7	GND	Ground	
8	DCDA DCD-	Data Carrier Detect Negative	Input
9	RXCB RXC+	Receive Clock Positive	Input
10	DCDB DCD+	Data Carrier Detect Positive	Input
11	TSETB TSET+	Transmit Signal Element Timing +	Output
12	TXCB TXC+	Transmit Clock Positive	Input
13	CTSB CTS+	Clear To Send Positive *	Input
14	TDB TX+	Transmit Positive	Output
15	TXCA TXC-	Transmit Clock Negative	Input
16	RDB RX+	Receive Positive	Input
17	RXCA RXC-	Receive Clock Negative	Input
18	LL	Local Loopback	Output
19	RTSB RTS+	Request To Send Positive	Output
20	DTRA DTR-	Data Terminal Ready Negative	Output
21	RL	Remote Loopback	Output
22	DSRB DSR+	Data Set Ready Positive	Input
23	DTRB DTR+	Data Terminal Ready Positive	Output
24	TSETA TSET-	Transmit Signal Element Timing -	Output



In External Sync Mode, the external sync signal is fed into CTS.

## RS-530A

Base+5, M3-M0=F, 1111

Pin #	Signal	Name	Mode
2	TDA TX-	Transmit Negative	Output
3	RDA RX-	Receive Negative	Input
4	RTSA RTS-	Request To Send Negative	Output
5	CTSA CTS-	Clear To Send Negative *	Input
6	DSRA DSR-	Data Set Ready Negative	Input
7	GND	Ground	
8	DCDA DCD-	Data Carrier Detect Negative	Input
9	RXCB RXC+	Receive Clock Positive	Input
10	DCDB DCD+	Data Carrier Detect Positive	Input
11	TSETB TSET+	Transmit Signal Element Timing +	Output
12	TXCB TXC+	Transmit Clock Positive	Input
13	CTSB CTS+	Clear To Send Positive *	Input
14	TDB TX+	Transmit Positive	Output
15	TXCA TXC-	Transmit Clock Negative	Input
16	RDB RX+	Receive Positive	Input
17	RXCA RXC-	Receive Clock Negative	Input
18	LL	Local Loopback	Output
19	RTSB RTS+	Request To Send Positive	Output
20	DTRA DTR-	Data Terminal Ready Negative	Output
21	RL	Remote Loopback	Output
24	TSETA TSET-	Transmit Signal Element Timing -	Output



In External Sync Mode, the external sync signal is fed into CTS.

## V.35 Signals

Base+5, M3-M0=E, 1110

Pin #	Signal	Name	V.35	Mode
2	TDA TX-	Transmit Negative	P	Output
3	RDA RX-	Receive Negative	R	Input
4	RTS	Request To Send	C	Output *
5	CTS	Clear To Send *	D	Input *
6	DSR	Data Set Ready	E	Input *
7	GND	Ground	B	
8	DCD	Data Carrier Detect	F	Input *
9	RXCB RXC+	Receive Clock Positive	X	Input
11	TSETB TSET+	Transmit Signal Element Timing +	W	Output
12	TXCB TXC+	Transmit Clock Positive	AA	Input
14	TDB TX+	Transmit Positive	S	Output
15	TXCA TXC-	Transmit Clock Negative	Y	Input
16	RDB RX+	Receive Positive	T	Input
17	RXCA RXC-	Receive Clock Negative	V	Input
18	LL	Local Loopback		Output *
20	DTR	Data Terminal Ready	H	Output *
21	RL	Remote Loopback		Output *
24	TSETA TSET-	Transmit Signal Element Timing -	U	Output



All modem control signals are single ended (un-balanced) with RS-232 signal levels. In External Sync mode, the external syn signal is fed into CTS.

# Technical Specifications

## Features

- Single synchronous/asynchronous serial port with DB-25M connector
- Configurable for RS-232, RS-422, RS-485, RS-530, RS-530A, or V.35
- Zilog Z85230 Enhanced Serial Communications Controller (ESCC™) supporting synchronous or asynchronous communications
- Programmable electrical interface selection for EIA/TIA-232/530/530A/485 and ITU V.35
- Programmable options for Transmit clock as input or output
- Software programmable baud rates
- Data rates to 128K bps
- MD1 Low Profile and Universal Bus (3.3V and 5V) compatible
- X.21 and RS-449 serial interface capability with versatile cabling options

## Environmental Specifications

Specification	Operating	Storage
Temperature Range	0° to 70° C (32° to 158° F)	-50° to 105° C (-58° to 221° F)
Humidity Range	10 to 90% R.H. Non-Condensing	10 to 90% R.H. Non-Condensing

## Power Consumption

Supply line	+5 VDC
Rating	350 mA

## Dimensions

Board length	4.72 inches (12.00 cm)
Board height	2.54 inches (6.45 cm)

## Manufacturing

All Sealevel Systems Printed Circuit boards are built to UL 94V0 rating and are 100% electrically tested. These printed circuit boards are solder mask over bare copper with either Electroless Nickel Immersion Gold (ENIG) or Hot Air Solder Leveled (HASL) finish.

# Appendix A – Troubleshooting

Following these simple steps can eliminate most common problems.

1. Read this manual thoroughly before attempting to install the adapter in your system.
2. Install software first. This places the required installation files in the correct locations. After installing the software, proceed to the physical installation section of this manual.
3. Make sure the Sealevel Systems adapter is securely installed.
4. Identify all I/O adapters currently installed in your system. This includes your on-board serial ports, controller cards, sound cards, etc. The I/O addresses used by these adapters, as well as the IRQ (if any) should be identified.
5. Configure your Sealevel Systems adapter so that there is no conflict with currently installed adapters. No two adapters can occupy the same I/O address.
6. Try the Sealevel Systems adapter with a unique IRQ. While the Sealevel Systems adapter does allow the sharing of IRQs, many other adapters (i.e., SCSI adapters and on-board serial ports) do not.
7. For Windows 2000/XP, the diagnostic tool 'WinSSD' is installed the SeaMAC folder on the Start Menu during the setup process. First find the ports using the Device Manager, then use 'WinSSD' to verify that the ports are functional.
8. Always use Sealevel Systems diagnostic software when troubleshooting a problem. This will eliminate any software issues from the equation.

If these steps do not solve your problem, please call Sealevel Systems' Technical Support, (864) 843-4343. Our technical support is free and available from 8:00 AM - 5:00 PM Eastern Time, Monday through Friday. For email support contact: [support@sealevel.com](mailto:support@sealevel.com).



# Appendix B – How To Get Assistance

Begin by reading through the Trouble Shooting Guide in Appendix A. If assistance is still needed please see below.

When calling for technical assistance, please have your user manual and current adapter settings. If possible, please have the adapter installed in a computer ready to run diagnostics.

Sealevel Systems provides an FAQ section on its web site. Please refer to this to answer many common questions. This section can be found at <http://www.sealevel.com/faq.asp>.

Sealevel Systems maintains a Home page on the Internet. Our home page address is [www.sealevel.com](http://www.sealevel.com). The latest software updates, and newest manuals are available via our FTP site that can be accessed from our home page.

Technical support is available Monday to Friday from 8:00 AM to 5:00 PM Eastern Time. Technical support can be reached at (864) 843-4343. For email support contact: [support@sealevel.com](mailto:support@sealevel.com).

**RETURN AUTHORIZATION MUST BE OBTAINED FROM SEALEVEL SYSTEMS BEFORE RETURNED MERCHANDISE WILL BE ACCEPTED. AUTHORIZATION CAN BE OBTAINED BY CALLING SEALEVEL SYSTEMS AND REQUESTING A RETURN MERCHANDISE AUTHORIZATION (RMA) NUMBER.**

# Appendix C – Electrical Interface

## RS-232

Quite possibly the most widely used communication standard is RS 232. This implementation has been defined and revised several times and is often referred to as RS 232 or EIA/TIA 232. It is defined by the EIA as the Interface between Data Terminal Equipment and Data Circuit- Terminating Equipment Employing Serial Binary Data Interchange. The mechanical implementation of RS 232 is on a 25 pin D sub connector. RS 232 is capable of operating at data rates up to 20 Kbps at distances less than 50 ft. The absolute maximum data rate may vary due to line conditions and cable lengths. RS 232 often operates at 38.4 Kbps over very short distances. The voltage levels defined by RS 232 range from -12 to +12 volts. RS 232 is a single ended or unbalanced interface, meaning that a single electrical signal is compared to a common signal (ground) to determine binary logic states. A voltage of +12 volts (usually +3 to +10 volts) represents a binary 0 (space) and -12 volts (-3 to -10 volts) denotes a binary 1 (mark). The RS-232 and the EIA/TIA-574 specification defines two type of interface circuits, Data Terminal Equipment (DTE) and Data Circuit-Terminating Equipment (DCE). The Sealevel Systems adapter is a DTE interface.

## RS-422

The RS-422 specification defines the electrical characteristics of balanced voltage digital interface circuits. RS 422 is a differential interface that defines voltage levels and driver/receiver electrical specifications. On a differential interface, logic levels are defined by the difference in voltage between a pair of outputs or inputs. In contrast, a single ended interface, for example RS 232, defines the logic levels as the difference in voltage between a single signal and a common ground connection. Differential interfaces are typically more immune to noise or voltage spikes that may occur on the communication lines. Differential interfaces also have greater drive capabilities that allow for longer cable lengths. RS 422 is rated up to 10 Megabits per second and can have cabling 4000 feet long. RS 422 also defines driver and receiver electrical characteristics that will allow 1 driver and up to 32 receivers on the line at once. RS 422 signal levels range from 0 to +5 volts. RS 422 does not define a physical connector.

## RS-485

RS-485 is backwardly compatible with RS 422; however, it is optimized for party line or multi drop applications. The output of the RS 422/485 driver is capable of being Active (enabled) or Tri State (disabled). This capability allows multiple ports to be connected in a multi drop bus and selectively polled. RS 485 allows cable lengths up to 4000 feet and data rates up to 10 Megabits per second. The signal levels for RS 485 are the same as those defined by RS 422. RS 485 has electrical characteristics that allow for 32 drivers and 32 receivers to be connected to one line. This interface is ideal for multi drop or network environments. RS 485 tri state driver (not dual state) will allow the electrical presence of the driver to be removed from the line. Only one driver may be active at a time and the other driver(s) must be tri stated. RS 485 can be cabled in two ways, two wire and four wire mode. Two wire mode does not allow for full duplex communication and requires that data be transferred in only one direction at a time. For half duplex operation, the two transmit pins should be connected to the two receive pins (Tx+ to Rx+ and Tx- to Rx-). Four wire mode allows full duplex data transfers. RS 485 does not define a connector pin out or a set of modem control signals. RS 485 does not define a physical connector.

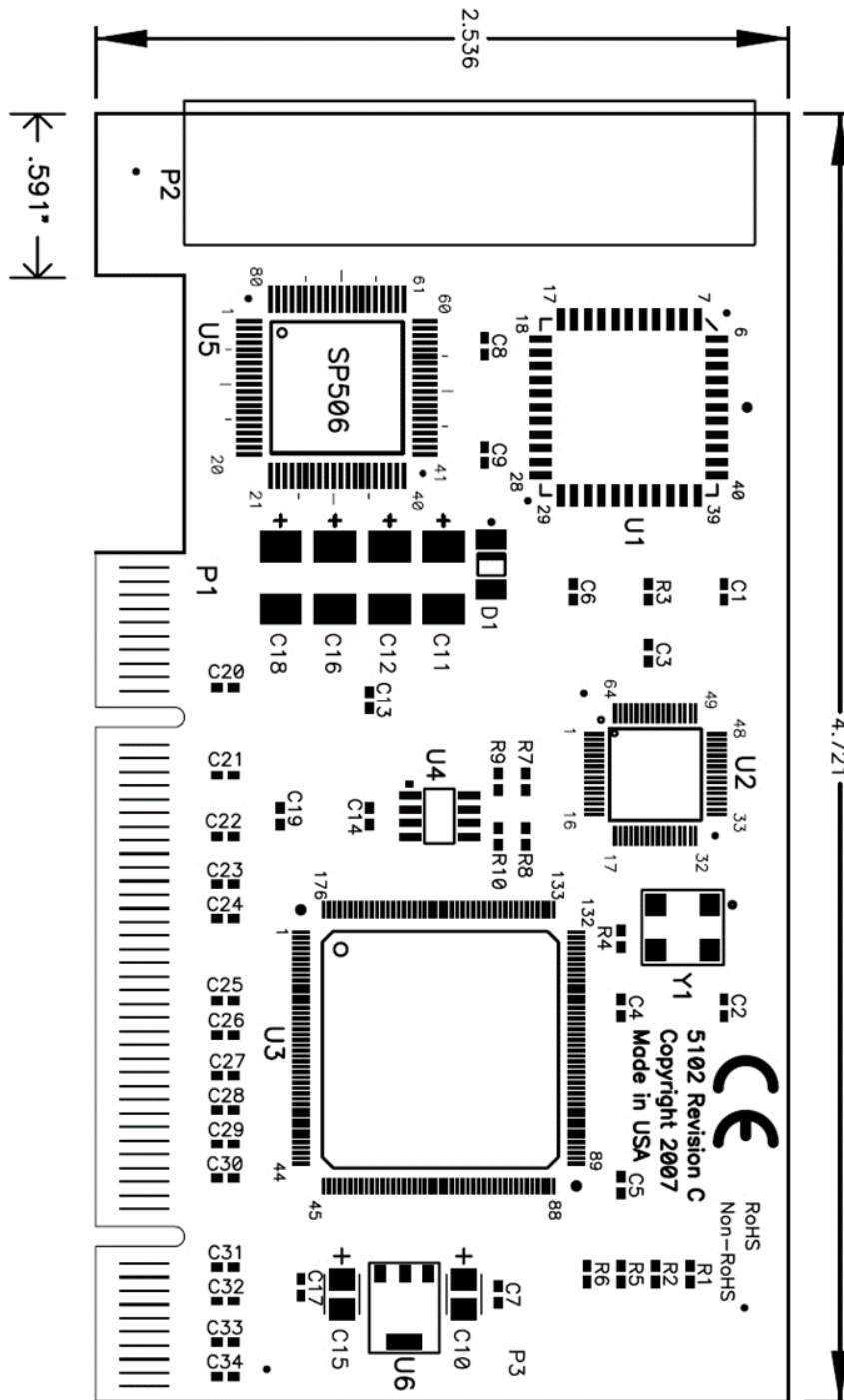
## RS-530/530A

RS-530 (a.k.a. EIA 530) compatibility means that RS 422 signal levels are met, and the pin out for the DB 25 connector is specified. The EIA (Electronic Industry Association) created the RS 530 specification to detail the pin out and define a full set of modem control signals that can be used for regulating flow control and line status. The major difference between RS-530 and RS-530A lies in some of the modem control interface signals. In RS-530 the signals all of the modem control signals are differential, in RS-530A some of these signals are single ended. The RS 530 specification defines two types of interface circuits, Data Terminal Equipment (DTE) and Data Circuit-Terminating Equipment (DCE). The Sealevel Systems adapter is a DTE interface.

## V.35

V.35 is a standard defined by ITU (formerly CCITT) that specifies an electrical, mechanical, and physical interface that is used extensively by high-speed digital carriers such as AT&T Dataphone Digital Service (DDS). ITU V.35 is an international standard that is often referred to as *Data Transmission at 48 Kbps Using 60 - 108 KHz Group-Band Circuits*. ITU V.35 electrical characteristics are a combination of unbalanced voltage and balanced current mode signals. Data and clock signals are balanced current mode circuits. These circuits typically have voltage levels from 0.5 Volts to -0.5 Volts (1 Volt differential). The modem control signals are unbalanced signals and are compatible with RS-232. The physical connector is a 34 pin connector that supports 24 data, clock, and control signals. The physical connector is defined in the ISO-2593 standard. ITU V.35 specification defines two type of interface circuits, Data Terminal Equipment (DTE) and Data Circuit-Terminating Equipment (DCE). The Sealevel Systems adapter is a DTE interface.

# Appendix D – Silk Screen – 5102 PCB



# Appendix E – Compliance Notices

## Federal Communications Commission (FCC) Statement



This equipment has been tested and found to comply with the limits for Class A digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference. In such case the user will be required to correct the interference at his own expense.

## EMC Directive Statement



Products bearing the CE Label fulfill the requirements of the EMC directive (89/336/EEC) and of the low-voltage directive (73/23/EEC) issued by the European Commission. To obey these directives, the following European standards must be met:

- **EN55022 Class A** - “Limits and methods of measurement of radio interference characteristics of information technology equipment”
- **EN55024** – “Information technology equipment Immunity characteristics Limits and methods of measurement”.

## United Kingdom Conformity Assessed Statement



Products with UKCA marking are in conformity with the essential requirements of the UK Electromagnetic Compatibility Regulations 2016:

- *Equipment must be designed and manufactured to ensure that the electromagnetic disturbance generated does not exceed the level above which radio and telecommunications equipment cannot operate as intended.*
- *The equipment has a level of immunity to the electromagnetic disturbance to be expected in its intended use which allows it to operate without unacceptable degradation of its intended use.*



This is a Class A Product. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.



Always use cabling provided with this product if possible. If no cable is provided or if an alternate cable is required, use high quality shielded cabling to maintain compliance with FCC/EMC directives.

# Warranty

Sealevel's commitment to providing the best I/O solutions is reflected in the Lifetime Warranty that is standard on all Sealevel manufactured I/O products. We are able to offer this warranty due to our control of manufacturing quality and the historically high reliability of our products in the field. Sealevel products are designed and manufactured at its Liberty, South Carolina facility, allowing direct control over product development, production, burn-in and testing. Sealevel achieved ISO-9001:2015 certification in 2018.

## Warranty Policy

Sealevel Systems, Inc. (hereafter "Sealevel") warrants that the Product shall conform to and perform in accordance with published technical specifications and shall be free of defects in materials and workmanship for the warranty period. In the event of failure, Sealevel will repair or replace the product at Sealevel's sole discretion. Failures resulting from misapplication or misuse of the Product, failure to adhere to any specifications or instructions, or failure resulting from neglect, abuse, accidents, or acts of nature are not covered under this warranty.

Warranty service may be obtained by delivering the Product to Sealevel and providing proof of purchase. Customer agrees to ensure the Product or assume the risk of loss or damage in transit, to prepay shipping charges to Sealevel, and to use the original shipping container or equivalent. Warranty is valid only for original purchaser and is not transferable.

This warranty applies to Sealevel manufactured Product. Product purchased through Sealevel but manufactured by a third party will retain the original manufacturer's warranty.

## Non-Warranty Repair/Retest

Products returned due to damage or misuse and Products retested with no problem found are subject to repair/retest charges. A purchase order or credit card number and authorization must be provided in order to obtain an RMA (Return Merchandise Authorization) number prior to returning Product.

## How to obtain an RMA (Return Merchandise Authorization)

If you need to return a product for warranty or non-warranty repair, you must first obtain an RMA number. Please contact Sealevel Systems, Inc. Technical Support for assistance:

Available	Monday – Friday, 8:00AM to 5:00PM EST
Phone	864-843-4343
Email	<a href="mailto:support@sealevel.com">support@sealevel.com</a>

## Trademarks

Sealevel Systems, Incorporated acknowledges that all trademarks referenced in this manual are the service mark, trademark, or registered trademark of the respective company.